# Research Article

# Stress Induced Effects for Advanced Polarization Control in Silicon Photonics Components

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We review the use of the oxide cladding stress-induced photoelastic effect to modify the polarization dependent properties in silicon-on-insulator (SOI) waveguide components, and highlight characteristics particular to this high index contrast (HIC) systems. The birefringence in SOI waveguides has its origin in the electromagnetic boundary conditions at the waveguide boundaries, and can be further modified by the presence of stress in the waveguiding materials. With typical stress levels in SiO<sub>2</sub> films, which are often used as the upper cladding, the waveguide effective index can be altered anisotropically up to the order of  $10^{-3}$  for ridges with heights ranging from 1  $\mu$ m to 5  $\mu$ m. This effect can be used effectively to counter the waveguide geometrical birefringence, allowing the waveguide cross-section profiles to be optimized for design criteria other than null geometrical birefringence. Design strategies are developed for using stress engineering to achieve a variety of functions. Polarization insensitive ring resonators, and polarization splitters and filters are demonstrated using these design principles.

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# 1. INTRODUCTION

Silicon photonics is a rapidly growing research field with many important advances in recent years [1–9]. Primarily motivated by the potential of high-integration density and compatibility with mature CMOS technologies, siliconon-insulator (SOI) has been the main material platform for silicon photonic waveguide components [1, 9]. Along with these benefits, control and utilization of polarizationdependent properties arise as a challenge, as well as open new possibilities in advanced designs and functionalities [10–15].

Polarization-dependent properties have long been an important issue in integrated optics systems. In applications such as wavelength demultiplexing and high resolution spectroscopy, the shift in channel wavelength with the polarization state of the incoming optical signal often compromises the device spectral resolution [8]. One approach to handle this issue is to produce devices with polarization insensitive performance. In some cases this approach may not be practical, and then polarization diversity may be adopted where the signal is divided into orthogonal polarization states using a polarization splitter and processed separately. In this paper, we review the characteristics of modal birefringence in SOI waveguides, with regard to the two main sources of polarization anisotropy, namely, the geometrical and the stress-induced birefringence [13, 16]. Design methodologies and experimental demonstrations for using the stress-induced birefringence to achieve polarization-insensitive arrayed waveguide gratings (AWGs) [13, 17], polarization insensitive ring resonators [18, 19], and zero-order AWG-based broadband polarization splitters [20] are presented.

#### 2. WAVEGUIDE BIREFRINGENCE: GEOMETRICAL EFFECTS

In planar waveguides, the modal birefringence (the difference in the effective index for the TM and TE polarizations  $\Delta n_{\rm eff} = n_{\rm eff}^{\rm TM} - n_{\rm eff}^{\rm TE}$ ) results from the combination of geometrical, compositional, and stress-induced effects. In low index contrast glass waveguides, the geometrical effect is negligible and the birefringence is primarily controlled by the residual stress in the waveguide layers. A large body of research has been devoted to this subject, where the main



FIGURE 1: Schematic cross-section of an SOI ridge waveguide.

goal is to reduce the stress anisotropy in the waveguide core region by adjusting the thermal expansion coefficients of the cladding and core layers or creating stress relief features [21, 22]. In high-index contrast waveguides such as SOI where the light confinement is strong, electromagnetic boundary conditions dictated by the cross-section geometry of the waveguides have the largest impact on the waveguide effective index and consequently the birefringence. These geometrical dependencies have been used to obtain single mode waveguides with large cross-sections, and to adjust the waveguide geometrical birefringence  $\Delta n_{geo}$  to desired values. In an SOI ridge waveguide as illustrated in Figure 1, it is possible to minimize the birefringence by tailoring the waveguide aspect ratio, particularly for waveguides with relatively large cross-sections [10-13]. As the core size is reduced, typically to  $\sim 2 \,\mu m$  and less, it becomes increasingly difficult to attain the designed birefringence values [13, 16, 17]. Since ridge dimensions also determine the number of waveguide modes, the minimum usable bend radius, the mode size, and the coupling between adjacent waveguides, it is often impossible to meet these different design objectives simultaneously.

As an example, Figure 2(a) (dashed lines) shows the dependence of waveguide geometrical birefringence  $\Delta n_{\rm geo}$ on the etch depth D for waveguides with vertical sidewalls, ridge height  $H = 2.2 \,\mu\text{m}$ , and widths of 1.6 and 2.5  $\mu\text{m}$ , respectively. Results for waveguides under stress (solid lines) will be discussed in Section 3. For a given waveguide width *W*, the polarization degeneracy ( $\Delta n_{geo} = 0$ ) may be achieved at a specific value of etch depth D. At the birefringence-free point for  $W = 1.6 \,\mu\text{m}$  near  $D = 1.3 \,\mu\text{m}$  the change of  $\Delta n_{\text{geo}}$ with the fluctuation in the etch depth is  $\sim 1.5 \times 10^{-4}$  for  $\Delta D = \pm 10$  nm. When  $W = 2.5 \,\mu m$ ,  $\Delta n_{geo}$  is less sensitive to the fluctuations in the etch depth ( $\sim 2 \times 10^{-5}$  for  $\Delta D$  =  $\pm 10$  nm near  $D = 1.5 \ \mu$ m), however the birefringence-free condition cannot be achieved. To reduce the birefringence below the level required for the state-of-the-art photonic devices  $(\Delta n_{\rm eff} < 10^{-4})$  by selecting and attaining the ridge aspect ratio W/D, dimension control on the order of 10 nm is required for these waveguides [13, 16, 17], which is very difficult in fabrication.

The effect of changing the waveguide width on the geometrical birefringence  $\Delta n_{geo}$  is shown in Figure 2(b) (dashed curves), for sidewall angles of 90° and 54°, which represent the profiles typically obtained by dry and

anisotropic wet etching, respectively. Waveguide sidewall angles strongly affect the mode properties, especially as the sidewall approaches vertical. With decreasing ridge width for  $\theta = 90^{\circ}$ ,  $\Delta n_{geo}$  shifts towards positive values initially, until the waveguide is so narrow that the modes mainly reside in the etched slab section and the birefringence changes to negative values rapidly. The birefringence of a trapezoidal ridge waveguide is less susceptible to changes in the ridge geometry (width and etch depth) as compared to a rectangular waveguide (i.e.,  $\theta = 90^{\circ}$ ) with similar cross-section area, as can be observed from the data for a sidewall angle of 54° shown in Figure 2(b). The geometrical birefringence, however, remains negative for the entire waveguide width range. For the rectangular waveguides, an optimal width can be found to satisfy the birefringence-free condition (zero birefringence at  $W \sim 2 \mu m$ ). Detailed discussions on the influence of geometrical parameters can be found in previous publications [16, 17].

Even though waveguide geometries satisfying both the single-mode and birefringence-free conditions can be found in principle, it is important to be able to implement the designs using existing fabrication technologies. Using contact-print lithography, waveguide-width variations on the order of 100 nm can be expected. Using state-of-the-art high resolution lithography methods such as e-beam direct write or deep UV steppers, dimensional control on the order of 10 nm is possible but still very challenging. These techniques, however, are expensive and are not widely available. Because it is difficult to precisely control the waveguide dimensions due to limitations of the fabrication techniques, methods of controlling birefringence other than modifying waveguide geometries are desirable.

## 3. PHOTOELASTIC EFFECT AND THE STRESS-INDUCED BIREFRINGENCE

Cladding layer such as silicon dioxide is usually deposited or grown at elevated temperatures on the silicon substrate. A (compressive) stress is often found in the silicon dioxide (SiO<sub>2</sub>) films, due to the different thermal expansion coefficients of Si  $(3.6 \times 10^{-6} \text{ K}^{-1})$  and oxide  $(5.4 \times 10^{-7} \text{ K}^{-1})$  as well as intrinsic stress that may exist in the oxide film. When a material is subjected to a stress, the material refractive index is altered due to the photoelastic effect. The stressinduced changes in the *material* refractive index are given by [13, 14, 23]

$$n_x - n_0 = -C_1 \sigma_x - C_2 (\sigma_y + \sigma_z),$$
  

$$n_y - n_0 = -C_1 \sigma_y - C_2 (\sigma_z + \sigma_x).$$
(1)

Here  $\sigma_i$  (i = x, y, z) is the stress tensor,  $n_i$  is the material refractive index in the corresponding directions,  $n_0$  is the (isotropic) refractive index in the absence of stress, and  $C_1$  and  $C_2$  are the stress-optic constants. If a stress (tensor) with axial anisotropy is imposed on an originally optically isotropic material, a *material* birefringence is induced:

$$n_y - n_x = (C_2 - C_1)(\sigma_y - \sigma_x).$$
 (2)



FIGURE 2: (a) Calculated modal birefringence  $\Delta n_{\text{eff}}$  for waveguides with  $H = 2.2 \,\mu\text{m}$ . (a)  $\Delta n_{\text{eff}}$  in ridge waveguides with vertical sidewalls  $(\theta = 90^\circ)$  as a function of etch depth *D*. Waveguide width W = 1.6 and  $2.5 \,\mu\text{m}$ , cladding thickness  $t_c = 0.7 \,\mu\text{m}$ , and film stress  $\sigma_{\text{film}} = -100 \,\text{MPa}$ . (b)  $\Delta n_{\text{eff}}$  in vertical ( $\theta = 90^\circ$ ) and trapezoidal ( $\theta = 54^\circ$ ) waveguides as a function of the ridge top width  $W_1$ , for  $D = 1.5 \,\mu\text{m}$ ,  $t_c = 0.7 \,\mu\text{m}$ , and  $\sigma_{\text{film}} = -300 \,\text{MPa}$ . The dashed curves show the geometrical birefringence  $\Delta n_{\text{geo}}$  (i.e., in the absence of stress), the solid curves show  $\Delta n_{\text{eff}}$  including both the geometrical and stress-induced components.

TABLE 1: Stress-optic constants of some materials.

Wateriai	$\pi (\mu \Pi I)$	$C_1(10^{-12} \text{ Pa}^{-1})$	$C_2 (10^{-12} \text{ Pa}^{-1})$
Si	1.15	-11.04	4.04
Silica	0.633	0.65	4.5
GaAs	1.15	-18.39	-10.63

Stress-optic constants of silicon and glass are listed in Table 1, as well as those of GaAs for comparison.

The stress-induced material birefringence in turn causes a stress contribution  $\Delta n_s$  to the modal birefringence. The total modal birefringence can be expressed as  $\Delta n_{\rm eff} = n^{\rm TM}$  $n^{\text{TE}} = \Delta n_{\text{geo}} + \Delta n_{\text{s}}$ . The stress distributions in an SOI ridge waveguide are shown in Figures 3(a) and 3(b), for SiO<sub>2</sub> upper and lower claddings under a compressive stress of  $\sigma_{\rm film}$  = -320 MPa ( $\sigma_{\rm film}$  is the in-plane stress component in the uniform cladding film far from the ridge), calculated using a finite-element multiphysics solver (FEMLAB). The primarily in-plane stress (Figure 3, x-direction) in the oxide film compresses the Si ridge and results in a compressive stress in the x-direction (Figure 3(a)) and a higher tensile stress in the ydirection (Figure 3(b)) in the silicon core. For the waveguide example shown in Figure 3,  $\sigma_x \approx -70$  MPa and  $\sigma_y \approx$ 180 MPa at the waveguide center. This stress anisotropy is the fundamental reason for the stress-induced birefringence. The corresponding local *material* birefringence  $(n_y - n_x)$  is shown in Figure 3(c), with values as large as  $4 \times 10^{-3}$ . These modifications in the *material* index cause the modal index  $n_{\text{eff}}^{\text{TM}}$  to increase and  $n_{\text{eff}}^{\text{TE}}$  to decrease with the oxide thickness and the stress level (Figure 4(a)). The corresponding stress-induced birefringence thus can be controlled with two parameters:

the oxide thickness  $t_c$  and the film stress level  $\sigma_{film}$ , as shown in Figure 4(b). The stress-induced index variations resulting from commonly used oxide cladding films are of comparable magnitude to the geometrical birefringence found in typical SOI ridge waveguides. Depending on the specific value of the geometrical birefringence  $\Delta n_{geo}$ , the total modal birefringence may be designed to be zero or other desired values. Figure 5 shows the calculated results for maximum birefringence that can be induced by a cladding with  $\sigma_{film} = -300$  MPa in waveguides with  $2 \,\mu$ m ridge height and different aspect ratios. These characteristics are the bases for stress engineering in SOI waveguides, and they can be advantageously employed in many applications.

#### 4. EXPERIMENTAL IMPLEMENTATIONS

In the following, we present several examples of using stress engineering for the control of polarization-dependent properties of SOI waveguide devices. First, we present polarization insensitive AWG demultiplexers, where the birefringence in the waveguide array is eliminated by the proper selection of the oxide cladding stress and thickness [13, 17]. Then we introduce polarization insensitive ring resonators using multimode interference (MMI) couplers to achieve polarization insensitive coupling between the bus waveguide and the ring cavity [18, 19]. Stress-induced birefringence is used to compensate the round-trip phase difference between the two polarizations. The last example is a polarization splitter in a zero-order AWG configuration, where a prism-shaped oxide patch is placed in the arrayed waveguide section to disperse the TE and TM modes into



FIGURE 3: Stress distributions in the (a) *x*-direction and (b) *y*-direction. The ridge height is 2.2  $\mu$ m, the width is 1.8  $\mu$ m, and the etch depth is 1.47  $\mu$ m. The cladding oxide is 1  $\mu$ m thick with a compressive stress of  $\sigma_{\text{film}} = -320$  MPa. (c) Stress-induced material birefringence ( $n_y - n_x$ ) corresponding to the stress distributions shown in Figures 3(a) and 3(b).



FIGURE 4: (a) Effective index as a function of the oxide upper-cladding thickness  $t_c$ , for different cladding stress levels  $\sigma_{\text{film}}$  of 0, -200, and -400 MPa, respectively; (b) total modal birefringence  $\Delta n_{\text{eff}}$  as a function of  $t_c$  and  $\sigma_{\text{film}}$ .



FIGURE 5: Stress-induced modal birefringence  $\Delta n_{\rm s}$  for ridge waveguides with different aspect ratios, with  $H = 2 \,\mu$ m,  $t_{\rm c} = 2 \,\mu$ m, and  $\sigma_{\rm film} = -300$  MPa.

separate output waveguides. Since this AWG operates at near zero-order, broadband performance is achieved [20].

#### 4.1. Polarization-independent AWGs

In AWGs, the polarization-dependent wavelength shift  $\Delta \lambda = \lambda_{TM} - \lambda_{TE}$  mainly arises from the birefringence of the arrayed

waveguides and can be expressed as  $\Delta \lambda \approx \lambda \Delta n_{\rm eff}/n_{\rm g}$ , where  $n_{g}$  is the waveguide group index and  $\lambda$  is the operating wavelength in vacuum. The AWGs used for this demonstration of polarization compensation using stress engineering were made on SOI substrates with a silicon thickness of  $2.2 \,\mu m$ , with 9 output channels spaced at 200 GHz, and centered at 1550 nm [8, 13, 17] (see Figure 6(a)). The arrayed waveguide gratings of order 40 were formed by 100 waveguides of width  $2\,\mu$ m. Waveguides were produced using both wet and dry etching methods, yielding ~54° and nearly vertical sidewall angles, respectively. Waveguide birefringence simulations were performed using measured waveguide dimensions. Upper cladding oxide films of different thicknesses were deposited at ~400°C using PECVD, with a film stress of  $\sigma_{\rm film} = -320 \,\text{MPa}$  in the blank films, measured from wafer bowing. Figure 6(b) shows the calculated and measured dependence of  $\Delta \lambda$  on the cladding thickness  $t_c$ , for both trapezoidal and rectangular waveguides. Calculated results agree well with experiments for both types of waveguides, showing that  $\Delta\lambda$  can be modified over ~2 nm wavelength range [16]. Figures 6(c) and 6(d) show the TE and TM spectra of a wet-etched AWG before and after



FIGURE 6: (a) Optical image of a fabricated AWG. (b) Polarization-dependent wavelength shift  $\Delta\lambda$  as a function of the cladding thickness  $t_c$ , for AWGs fabricated using wet (waveguide top width  $W_1 = 1.1 \,\mu$ m and bottom width  $W_2 = 3.8 \,\mu$ m) and dry ( $W_1 = 1.85 \,\mu$ m and  $W_2 = 2.0 \,\mu$ m) etching to a depth of  $D = 1.47 \,\mu$ m. The cladding stress is  $\sigma_{\text{film}} = -320 \,\text{MPa}$ . Measured spectra for an SOI AWG. (c) Measured spectrum for an SOI AWG without the oxide upper cladding. (d) Compensated using a 0.6  $\mu$ m thick SiO<sub>2</sub> claddings with  $\sigma_{\text{film}} = -320 \,\text{MPa}$ . TM (solid) and TE (dashed).

polarization compensation. The adjacent channel cross-talk is less than -25 dB. The noncompensated AWG device has a polarization-dependent wavelength shift of  $\Delta\lambda \sim 0.6$  nm (Figure 6(c)), arising from the geometrical birefringence of  $\Delta n_{\rm geo} \sim -1.3 \times 10^{-3}$  in the arrayed waveguide. With an oxide upper cladding film of -320 MPa in stress and  $0.6 \,\mu$ m in thickness, the  $\Delta\lambda$  was then reduced to below 0.04 nm for all channels (corresponding to  $\Delta n_{\rm eff} < 1 \times 10^{-4}$ ) (Figure 6(d)). The polarization dependent loss (PDL) was also found to be negligible in these devices after the compensation.

#### 4.2. MMI-coupled polarization-independent ring resonators

We also applied the stress engineering technique to achieve polarization-insensitive ring resonators [18, 19]. Ring res-

onators have been used as the building blocks in applications including add-drop, switching, modulation, and sensing [24–27]. However, polarization sensitivity is often an obstacle that limits their practical use [12, 15, 18].

The power transmission in the bus waveguide of a single coupler ring resonator (Figure 7(a)) can be obtained by a generalization of the ring resonator equations to incorporate the coupler loss [18, 28]:

$$|T|^{2} = \alpha_{\text{MMI}}^{2} \left[ \frac{\alpha^{2} - 2 \,\alpha t \cos\phi + t^{2}}{1 - 2 \,\alpha t \cos\phi + \alpha^{2} t^{2}} \right], \tag{3}$$

where  $\phi$  is the total round trip phase accumulation, and  $\alpha^2 = \alpha_{\text{MMI}}^2 \cdot \alpha_{\text{ring}}^2$  is the combined power loss factor including both the ring loss and the coupler loss. The self-coupling



FIGURE 7: (a) A ring resonator with an MMI coupler. (b) A schematic of the MMI structure.

coefficient  $t^2$  is the ratio of power transfer from the input to the output port 1 of the multimode interference (MMI) coupler, as indicated in Figure 7(b). The resonance extinction ratio is maximum at  $\alpha^2 = t^2$ , which is referred to as the critical coupling condition. A series of transmission minima occur at wavelengths  $\lambda_m$  for which the round-trip phase accumulation is integer multiple of  $2\pi$ . It is apparent that a polarization dependence in the coupling coefficient tand/or the loss factor  $\alpha$  lead to different resonance quality factors and linewidths for TE and TM input polarizations. If the waveguides are birefringent, the round trip phase accumulation  $\phi$  varies with the polarization, and the resonance wavelengths and free spectral range (FSR) are polarization sensitive. To achieve polarization independence in a ring resonator, all these factors need to be addressed.

Polarization-independent ring resonator designs using directional couplers have been investigated [12], but showed long coupler length and very stringent fabrication requirements. We proposed the use of MMI couplers to achieve polarization-independent light transfer between the resonator bus and the ring waveguides (Figures 7(a) and 7(b)). For simplicity, we have chosen the commonly used  $2 \times 2$ restricted interference coupler with a 50:50 splitting ratio between the two output ports [29]. The coupler width is  $W_{\rm MMI} = 3D$ , where D is the center-to-center separation between the access waveguides (Figure 7(b)). The resonators are designed with the following ring and access waveguide dimensions: ridge height  $H = 1.5 \,\mu\text{m}$ , width  $W = 1.5 \,\mu\text{m}$  and  $1 \,\mu\text{m}$ , and etch depth  $H - h = 0.9 \,\mu\text{m}$ . A separation of D = $2.5\,\mu\text{m}$  between the access waveguides is used in the MMI design, based on the considerations of a compact footprint and ease of fabrication, yielding an MMI width of  $W_{\rm MMI}$ = 7.5  $\mu$ m. The quality factor of a resonator is determined by the coupler splitting ratio and the loss in the resonator cavity. MMI designs with variable splitting ratios have been proposed and demonstrated by other groups and may be used for high-quality factor resonators [30, 31].

The optical field distribution in the 2 × 2 MMI coupler is shown in Figure 8(a), while Figure 8(b) shows the MMI coupler transfer characteristics in terms of the normalized power coupled to the fundamental mode in ports 1 and 2, respectively, as a function of the coupler length. For each polarization, a splitting ratio close to 50:50 is achieved over the range of coupler length  $L_{\rm MMI}$  from 82 to 86  $\mu$ m, with a coupler imbalance of ≤0.05 dB. The combined power in the two outputs is the total transmitted power  $\alpha_{\rm MMI}^2$ , which indicates the loss through the coupler (with  $\alpha_{MMI}^2$ = 1 being lossless). The optimum coupling length differs by  $\sim 2.5 \,\mu m$  (or  $\sim 3\%$ ) between the two polarizations. As a first approximation, a coupler length of  $L_{\rm MMI} = 84 \,\mu {\rm m}$ at the crossing point is used in all our resonator designs, where a similar excess loss of  $\sim -0.2 \text{ dB}$  is found for both polarizations. This choice is based on the assumption that losses from all other mechanisms are similar for TE and TM, which is not necessarily the case. In addition to the coupler excess loss, there are several other mechanisms contributing to the overall resonator loss. For example, the waveguide propagation loss due to scattering depends on the optical mode overlap with the roughness at the waveguide boundaries and is generally polarization dependent. This loss is proportional to the propagation distance, and therefore to the cavity length of a ring resonator. Another source is the radiation loss in waveguide bends, as well as possible losses at discontinuities such as the junctions between the straight and curved waveguides. These factors need to be considered in the device design, and detailed discussions can be found in [19].

Since the stress in SOI waveguides caused by the cladding layer can be used effectively to control the birefringence over a wide range, it can be applied to eliminate the polarization dependence in the ring phase accumulation  $\phi$ . The phase change in the MMI section is  $2\pi n_{\text{eff}}^{\text{MMI}} L^{\text{MMI}}/\lambda$ , plus a constant correction factor which is a multiple integer of  $\pi/2$  [32]. Since the stress-induced birefringence in the ring waveguide  $\Delta n_{\text{s}}^{\text{ring}}$  is much larger than that in the wider MMI section [16] (i.e.,  $\Delta n_{\text{s}}^{\text{MMI}} \ll \Delta n_{\text{s}}^{\text{ring}}$ ), the difference in the phase accumulation  $\Delta \phi$  between TM and TE polarizations can be expressed as [18]:

$$\Delta \phi = \frac{2\pi}{\lambda} \left[ \Delta n_{\text{geo}}^{\text{MMI}} L_{\text{MMI}} + \Delta n_{\text{geo}}^{\text{ring}} L_1 + \Delta n_{\text{s}}^{\text{ring}} L_1 \right].$$
(4)

Here  $L_1$  is the total length of the narrower ring waveguide sections,  $L_1 = L - L_{\text{MMI}} = 2\pi R + L_{\text{MMI}}$ . Figure 9 shows the ring resonator phase difference  $\Delta \phi = \phi^{\text{TM}} - \phi^{\text{TE}}$ as a function of the oxide cladding thickness for two cladding stress levels. Obviously, various combinations of cladding stress and thickness can be found to adjust  $\Delta \phi$  to zero.

We have fabricated MMI-based ring resonators in SOI wafers with  $1.5 \,\mu$ m thick silicon at the Canadian Photonics Fabrication Center (CPFC). SEM images of a fabricated device are shown in Figures 10(a) and 10(b). Standard inhouse PECVD process produces oxide films with -250 MPa stress. Measured transmission spectra for a MMI-coupled ring resonator with a radius of 200  $\mu$ m are shown in Figure 10(c) for TE and TM polarizations. Over the 4 nm scan range, the polarization-dependent wavelength shift is below 2 pm, which is the laser scan step used in these measurements. The measured free spectral range (FSR) is ~0.46 nm, and the quality factor is  $Q \sim 15,000$ . Polarization insensitive operation is achieved in these resonators, demonstrating the effectiveness of the proposed method for polarization control.



FIGURE 8: (a) Optical field distribution in the SOI MMI coupler of  $7.5 \,\mu\text{m} \times 84 \,\mu\text{m}$  in dimensions, with an etch depth of  $1 \,\mu\text{m}$ . (b) Corresponding normalized power in the fundamental mode in ports 1 and 2, for the TE and TM polarizations.



FIGURE 9: Phase difference  $\Delta \phi = \phi^{TM} - \phi^{TE}$  as a function of the oxide cladding thickness for an MMI-coupled ring resonator with a radius *R* of 200  $\mu$ m and an oxide cladding stress of -220 and -250 MPa, respectively.

#### 4.3. Broadband polarization splitter in a zero-order AWG configuration

Stress-induced effects can also be used to produce high level of birefringence in selected waveguide areas. Since the stress-induced index modifications are several orders of magnitude smaller than the core-cladding refractive index contrast in SOI, the effect of a SiO<sub>2</sub> cladding on mode shape is negligible. Thus there is little mode mismatch loss or polarization-dependent loss at the junctions between waveguide sections with and without the cladding. Figure 11 shows a zero-order AWG where all of the arrayed waveguides have

identical geometrical path lengths [20]. A triangular patch of oxide cladding is placed in the arrayed waveguide section with a constant length increment  $\Delta L$  between the adjacent waveguides. The stress in the waveguide cladding induces a polarization-dependent phase difference in the light signal propagating in the waveguide array (see Figure 4(a)), causing a polarization-dependent tilt in the phase front of the light in the free propagation region (FPR) (see Figure 11(b)). Since all waveguides have the same physical length, the phase difference for light propagating in the adjacent waveguides of the arrayed section depends solely on the cladding stressinduced index change and the patch-length increment. Since the stress-induced index change in the TE and TM polarization modes has opposite signs for silicon waveguides, the two modes are spatially displaced in the opposite directions in the focal region relative to the free propagation region centerline, as shown in Figure 11(b). With proper placement of the two receiver waveguides along the focal plane, the two polarizations are spatially separated.

Zero-order AWG-based polarization splitters were designed and fabricated on SOI wafers, with a  $2.2 \,\mu m$  thick silicon core layer. The deposited oxide cladding film had a thickness of 1  $\mu$ m, and the measured cladding film stress was approximately -340 MPa. The overall device size is  $\sim$ 12 mm  $\times$  4 mm. Figure 12(a) shows several fabricated AWG devices with triangular cladding regions of varied patch sizes. Based on the measured cladding film stress and thickness, the cladding patch-length increment  $\Delta L$ between the adjacent waveguides was set to  $16.4 \,\mu\text{m}$  to produce the required polarization-dependent phase front tilt at the output of the waveguide array. Since stress-induced effects do not vary with wavelength appreciably, and the AWG operates close to zero order, the performance of the splitter is expected to be wavelength independent over a large application bandwidth, which was experimentally



FIGURE 10: (a) SEM image of an MMI-coupled ring resonator. (b) A closeup of the MMI section. (c) TE and TM transmission spectra of the ring resonator, with an upper SiO<sub>2</sub> cladding of 0.8  $\mu$ m thick and film stress of  $\sigma_{\rm film} = -250$  MPa.



FIGURE 11: (a) Schematic layout of a broadband zero-order AWG polarization splitter. (b) Geometry of the output FPR with a Rowland circle configuration.



FIGURE 12: (a) A top view of the fabricated wavelength-independent zero-order AWG polarization splitters and insert an expanded SEM image of the oxide cladding patch in the waveguide array. (b) Measured transmission as a function of wavelength of a zero-order AWG-based polarization splitter.

confirmed. The measured extinction ratio of the polarization splitter was better than -10 dB for both output polarizations over the entire tuning range of our laser (1465–1580 nm). Figure 12(b) shows the measured polarization splitter transmission for a wavelength range of  $\lambda = 1537$  to 1557 nm. Over this range, the splitter extinction ratio is <-15 dB. The best extinction ratio achieved was -20 dB. Similar birefringence modification method was also applied in a Mach-Zehnder interferometer configuration to form polarization splitters and filters [33].

## 5. SUMMARY

We have reviewed the stress-induced modifications of the waveguide effective index for the TE and TM modes and the associated birefringence which are important for a wide range of commonly employed SOI waveguide geometries. The significance of these stress-induced effects is now being recognized in the research community and applied in component development. If not taken into consideration, these effects can lead to large deviations in device characteristics from the designed specifications. Fortunately, the use of cladding stress to correct the waveguide birefringence allows a considerable degree of freedom in designing SOI waveguides to meet other performance criteria such as relaxed dimensional tolerance, reduced loss at waveguide bends, and overall improved device performance. This technique is simple to implement, and there are no additional process steps required other than the conventional cladding layer deposition. The stress-induced modifications to the effective index in SOI waveguides are readily controlled by the stress level and the thickness of the upper-oxide cladding. We have demonstrated that cladding stress engineering can be used to achieve polarization insensitivity in AWGs, spectrometers, and ring resonators, leaving the freedom of optimizing the waveguide geometry for considerations other than the birefringence. Since the effect of the SiO<sub>2</sub> cladding on the mode profile is negligible, there is little mode mismatch loss or polarization-dependent loss at the junctions between waveguide sections with and without the cladding. Therefore, tailored cladding patches can be applied at discrete locations in a planar waveguide circuit with negligible insertion loss and PDL penalty, as we demonstrated in the example of a broadband polarization splitter. The applications of cladding stress-induced effects can be envisioned in a variety of situations to enhance device functionality, simplify fabrication, and improve operation tolerance.

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