[1]张娇婷,张颖龙,朱 怡,等.基于ARM+FPGA的通用导弹弹载记录仪的设计[J].弹箭与制导学报,2014,2:38-41.

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Title: The Design of Universal Missile-borne Data Recorder Based on the Structure of ARM+FPGA

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摘要: 针对目前种类越来越多的武器系统科研背景,开发出一种通用型的导弹弹载记录仪以满足不同项目背景的测试要求变得十分必要,文中提出了一种基于结构的通用导弹弹载记录仪设计方案,以实现对各种不同电器接口信号的测试。详细介绍了这种方案的硬件结构设计和软件设计。此种设计方案合理可行,满足了通用导弹弹载记录仪实时性、大容量、多接口的设计要求。

Abstract: In light of more and more types of weapon system research background at present, it becomes very necessary to develop a generic type of missile borne recorder in order to satisfy the different background of the project testing requirements. In this paper, a design scheme of general missile borne recorder was presented based on the structure of ARM+FPGA in order to realize various electrical interface signal test. Also, in this paper, the hardware design and software design of the scheme were introduced. This design is reasonable and feasible, satisfies the general missile borne recorder's design requirements of real-time, large capacity and multi interface.

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