贺小勇 He Xiaoyong

基本信息

职称：微电子学院副教授、硕导

Title: Associate Professor, Graduate Supervisor, School of Microelectronics

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个人简历

贺小勇，研究方向: 模拟与混合信号集成电路、数字集成芯片、电源管理与传感芯片等。授权实用新型专利多项，发明专利多项公示中；发表论文20多篇；合编中英文对照教材一本。已培养校硕士毕业生12名。中央高校基本业务费，省市级及企业横向等项目多项。

HE XIAOYONG His research interest includes analog and mixed signal IC design, digital IC design, and sensor chip design etc. He has authorized a number of utility model patents and invention patents, published more than 20 papers, and co-authored a bilingual textbook in Chinese and English. He supervised 15 master students.

教育经历

2004.08-2009.10 香港中文大学电子工程系 博士

1997.09-2000.06 华中科技大学固体电子系 硕士

1993.09-1997.06 华中理工大学固体电子系 学士

工作经历

2012.12-　　华南理工大学 副教授

2009.10-2012.3　富士通半导体香港研发中心高级工程师

2000.07-2004.06 摩托罗拉半导体苏州设计中心设计工程师

研究方向

集成电路设计

授课课程

数字集成电路原理与设计

数字集成电路原理与设计课程设计

电子系统EDA设计

数字电子技术基础

代表性科研成果

1. Y. Zhang, **X.Y. He** and K.P. Pun, An Extremely Linear Multi-level DAC for Continuous-time Delta-Sigma Modulators, IEEE Transactions on Circuits and Systems II: Express Briefs, vol. 66, no. 3, March 2019.

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3. **He Xiaoyong**, Lu Tenghui, Jing Zhaoxia, “Design of Command Queuing Engine in eMMC5.1 Host System”, IEEE International Conference on Electron Devices and Solid-State Circuits (EDSSC) , 2017

4. **Xiaoyong He**, Peiyu Huang, Zhaohui Wu, Yaofeng Yan, Jianxin Yang and Yanqi Zheng, “Synchronous coarse-fine comparators based zero-current detector for DC-DC converter operating in switching frequency beyond 10 MHz”, IEEE International Conference on Electron Devices and Solid-State Circuits (EDSSC) , 2017

5. Jiatong Li, Sébastien Le Nours, **Xiaoyong He**, Zhaoxia Jing, “A Statistical Characterization Approach to Estimate Software Execution Time in Multiprocessor Systems”, ICITACEE2017

6. K. Xu, M. Cai, E. H. Dagher, B. Xu, W. K. Masenten, H. Ye, M. Huang and **X. Y. He**, “A 10.2mW Multi-mode Continuous-Time ΔΣ ADC with 70-87dB DR and 0.7-10MHz Bandwidth for TD-SCDMA and LTE Digital Receivers”, Analog Integr Circ Sig Process (2016) pp.395–410. (corresponding author)

7. **Xiaoyong He,** Junliang He, Min Cai; A Low Power Switching Method with Variable Comparator Reference Voltage and Split Capacitor Array for SAR ADC, IEEE International Conference on Electron Devices and Solid-State Circuits (EDSSC) PP.379-382, 2016

8. **Xiaoyong He,** Hui Yan, Min Cai; A Combined PTS-Companding Scheme for PAPR Reduction in OFDM System, to appear in *IEEE 2nd Advanced Information Technology, Electronic and Automation Control Conference 2017*

9. Fang Tang, Bo Wang, Amine Bermak, xichuan Zhou, Shengdong Hu, and **Xiaoyong He**, “A Column-Parallel Inverter-Based Cyclic ADC for CMOS Image Sensor With Capacitance and Clock Scaling”, *IEEE Trans. on Electron Devices, Vol. 63. No.1, pp.162-167, Jan 2016*

10. Weiguo Zheng, Min Cai, **Xiaoyong He**, Ken Xu, Zhijian Chen, “Analysis and design of quickly starting crystal oscillator,” *Proceedings 2015 11th IEEE International Conference on ASIC*, *pp.1071-1074, Chengdu China,Nov 3-6, 2015*

11. Zhaoxin Xiong, Ming Cai, **Xiao-yong He**, Yun Yang, “Digital Background Calibration for A 14-bit 100-MS/s Pipelined ADC”, *IEICE Transaction on Electronics,* vol . E97C, issue 3, pp.207-214, may. 2014 (corresponding author)

12. Hua Bin Zhang, Min Cai, **Xiaoyong He***, Haijun Wu, Zhengpin Li and F. Tang, “Broadband and high-K passive balun using 16 sides geometry for silicon-based RFICs”, *Electronics Letters*, Feb. 2014 Vol. 50 No. 4 pp. 288–289(corresponding author)

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