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## 丰光银 Feng Guangyin



### 基本信息

职称：副教授、博导、硕导

Title: Assistant Professor, Doctoral & Graduate supervisor

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## 招生专业



博士

【学术型】电子科学与技术（微电子学与固体电子学）



硕士

【学术型】电子科学与技术（微电子学与固体电子学）

【专业型】电子信息（集成电路工程）

## 个人简历

丰光银，博士毕业于新加坡南洋理工大学，主要研究方向有：用于通信和雷达的射频/毫米波集成电路与系统设计、无线电能传输。主持或参与广东省自然科学基金面上项目、琶洲实验室研究项目、新加坡教育部研究项目、新加坡国家基金委项目等，发表SCI和EI收录论文30余篇，其中JCR一区论文11篇，申请PCT发明专利1项。

Dr. Feng received the Ph.D. degree from Nanyang Technological University, Singapore. His main research fields are RF/millimeter wave integrated circuits and systems for communication and radar applications and wireless power transfer. Dr. Feng has presided over or participated in research projects from Guangdong Natural Science Foundation, Pazhou Laboratory, Singapore Ministry of Education, Singapore National Research Foundation, etc., published more

than 30 SCI/EI papers, 11 of them published in journals ranking in JCR Q1, and applied for one PCT international patent protection.

## 教育经历

2012.01–2016.01 新加坡南洋理工大学, 博士

2006.09–2010.06 东北大学, 学士

## 工作经历

2020.08–至今华南理工大学, 预聘助理教授 (副教授职称), 特聘研究员, 硕士生导师

2019.09–2020.05 华为新加坡研究所, 资深工程师

2016.04–2019.08 新加坡南洋理工大学, 博士后

## 研究方向

射频/毫米波集成电路与系统设计, 无线电能传输

## 授课课程

模拟集成电路原理与设计

## 学术任职

IEEE会员, 固态电路协会会员, 长期担任JSSC、TMTT、MWCL, IEEE Microwave Magazine、IEEE Access等国际期刊的审稿人。

## 科研项目

1. 广东省自然科学基金面上项目, 基于极化架构的硅基低功耗毫米波接收机关键技术研究, 2021/01–2023/12, 主持;
2. 人工智能与数字经济广东省实验室 (广州), 智能感知与无线传输芯片与系统, 2020/09–2023/08, 参与;
3. 华南理工大学中央高校基本科研业务费, 5G毫米波智能集成通信芯片设计技术及其应用, 2021/01–2022/12, 参与。

## 代表性科研成果 (论文、专利等)

[1] G. Feng, C. C. Boon, F. Meng, X. Yi, K. Yang, C. Li, and H. Luong, "Pole-Converging Intra-Stage Bandwidth Extension Technique for Wideband Amplifiers," *IEEE Journal of Solid-State Circuits*, vol. 52, no. 3, pp. 769–780, Mar. 2017. (JCR Q1)

[2] G. Feng, X. Yi, F. Meng, and C. C. Boon, "A W-Band Switch-Less Dicke Receiver for Millimeter-Wave Imaging in 65 nm CMOS," *IEEE Access*, vol. 6, pp. 39233–39240, Jul. 2018. (JCR Q1)

[3] G. Feng, C. C. Boon, F. Meng, and X. Yi, "A 100-GHz 0.21-K NETD 0.9-mW Charge-Accumulation Super-Regenerative Receiver in 65-nm CMOS," *IEEE Microwave and Wireless Components Letters*, vol. 26, no. 7, pp. 531–533, Jul. 2016. (JCR Q2)

- [4] **G. Feng**, C. C. Boon, F. Meng, X. Yi, and C. Li, "An 88.5-110 GHz CMOS Low-Noise Amplifier for Millimeter-wave Imaging Applications," *IEEE Microwave and Wireless Components Letters*, vol. 26, no. 2, pp. 134–136, Feb. 2016. (**JCR Q2**)
- [5] **G. Feng** and J. J. Sit, "An Injection-Locked WPT Transmitter with Automatic Maximum Efficiency Tracking," *IEEE Transactions on Industrial Electronics* (Accepted, **JCR Q1**).
- [6] K. Yang, X. Yi, C. C. Boon, Z. Liang, **G. Feng**, C. Li, and B. Liu, "A Parallel Sliding-IF Receiver Front-End with Sub-2 dB Noise Figure for 5-6 GHz WLAN Carrier Aggregation," *IEEE Journal of Solid-State Circuits*, vol. 56, no. 2, pp. 392–403, Aug. 2020. (**JCR Q1**)
- [7] X. Yi, **G. Feng**, Z. Liang, C. Wang, B. Liu, C. Li, K. Yang, and C. C. Boon, "A 24/77 GHz Dual-Band Receiver for Automotive Radar Applications," *IEEE Access*, vol. 7, pp. 48053–48059, Mar. 2019. (**JCR Q1**)
- [8] X. Yi, Z. Liang, **G. Feng**, F. Meng, C. Li, K. Yang, B. Liu, and C. C. Boon, "A 93.4-to-104.8 GHz 57 mW Fractional-N Cascaded Sub-Sampling PLL with True In-Phase Injection-Coupled QVCO in 65 nm CMOS," *IEEE Transactions on Microwave Theory and Techniques*, vol. 67, no. 3, pp. 2370–2381, Apr. 2019. (**JCR Q1**)
- [9] **G. Feng** and J. J. Sit, "Injection-Locked Power Oscillator for Resonance Frequency Tracking in Wireless Power Transfer," in *IEEE Biomedical Circuits and Systems Conferences*, Oct. 2018.
- K. Yang, C. C. Boon, **G. Feng**, C. Li, Z. Liu, X. Yi, Y. Dong, A. Zhou, X. Wang, "A 1.75dB NF 25mW 5GHz Transformer-Based Noise Cancelling CMOS Receiver Front-End," *International Solid-State Circuits Conference (ISSCC)*, Feb. 2021.

#### 本科生培养

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##### 师资概况

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##### 硕导

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