



博学而笃志 切问而近思

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主页:

教育背景

复旦大学电子工程系 本科学士
复旦大学电子工程系 硕士研究生
复旦大学材料系 博士研究生

研究方向

课程教学

博士生课程: 计算机体系结构
研究生课程: VLSI 集成技术原理

学术兼职

国家重大专项“核心电子器件，高端通用芯片和基础软件”实施专家组成员
863射频识别专项专家组成员

科研项目

论文著作

1. IEEE Asia Solid-State Circuits Conference, A Wideband CMOS Low-Noise Amplifier for TV Tuner Applications, , , 259-262
2. Proc. Of 7th International Conference on ASIC, 2007, A Novel Baseband-Processor for LF RFID Tag, , Volume.2, 870-873
3. IEEE Wireless Communications and Networking Conference 2007, Digital Correlation Demodulator Design for RFID Reader Receiver, , , 1664-1668
4. IEEE Radio Frequency Integrated Circuits (RFIC) Symposium, A fully integrated 1.175-to-2GHz frequency synthesizer with constant bandwidth for DVB-T applications, , 303-306 ,
5. International Solid-State Circuits Conference (ISSCC), A 975 to 1960 MHz, Fast-Locking Fractional-N Synthesizer with Adaptive Bandwidth Control and 4/4.5 Prescaler for DTV Tuners, , ,
6. IEEE International Solid-State Circuits Conference, accepted, Low-power Low-cost

UHF Tag with On Chip Antenna, , ,

7. The 3rd IEEE Asia Solid-State Circuits Conference Proc. ASSCC 2007 No. 9-3, A High Efficiency AC-DC Charge Pump Using Feedback Compensation Technique, , , 252-255

8. IEEE Wireless Communications & Networking Conference, 2007, A new architecture of UHF RFID digital receiver for SoC implementation, , , 1659-1663

9. Applications and the Internet Workshops, 2006. SAINT Workshops 2006.

International Symposium, Design of low-power baseband-processor for RFID tag, , , 4 pp

10. Circuits and Systems, 2006. ISCAS 2006. Proceedings. 2006 IEEE International Symposium, Low-complexity synchronization technique with adaptive mode detection for DVB-H system, , , 4 pp