基于32位数字信号处理器和16位同步串行模数转换器的配用电监 控终端设计

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摘要

采用32位控制型数字信号处理器、32位嵌入式先进精简指令集处理器和具有16位精度的同步采样串行接口模数转换器,设计并实现了全隔离的配用电监控终端。在设计中使用多重软硬件抗干扰措施,提高了装置的可靠性;应用软硬件缓冲技术和优化的历史数据查询算法提高了系统效率。采用GPRS作为通信手段,并对其应用可靠性进行了深入研究和实践。在定点数字信号处理器中采用C语言编程,提高了系统的可靠性和可维护性。

关键词 配用电;监控终端;32位数字信号处理器;嵌入式先进精简指令集处理器;16位同步串行模数转换器;抗干扰;可靠性;缓冲技术;通用分组无线业务(GPRS)

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Design of Distribution and Utilization Monitoring Terminal Based on 32-bit Digital Signal Processor and 16-bit Synchronous Serial A/D Converter

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Abstract

By using of 32-bit digital signal processor (DSP), embedded Advanced RISC Machine (ARM) and 16-bit synchronous serial A/D converter, a full isolated distribution and utilization monitoring terminal is designed and implemented. In this design multiple anti-interference measures realized by hardware and software are adopted to improve the reliability of this device; the buffer technique based on hardware and software as well as the optimized query algorithm for historical data are applied to improve system efficiency. The General Packet Radio Service (GPRS) module is taken as communication means and the reliability of its application is further researched and practiced. For fixed-point DSP, the C language programming is adopted, thus both reliability and maintainability of the system are enhanced.

Key words <u>distribution and utilization; monitor and control unit; 32-bit</u> DSP; embedded advanced RISC machine (ARM); 16-bit synchronous serial A/D; anti-interference; reliability; buffer technology; general packet radio service (GPRS)

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