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论文

智能变电站中采样值传输延时的处理

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摘要:

将模拟量采样值(sampled analogue value, SAV)报文准确、实时地传输至智能电子设备是智能变电站过程总线数据通信的关键内容。为此在建立SAV报文传输时序模型的基础上,提出一种采用合并单元相位校准补偿传输延时以提高模拟采样值时标的准确性,并通过测算以太网的最大传输延时验证SAV报文的实时性的方法。利用公式推导和算例计算对方法进行了理论分析,并通过OPNET modeler仿真验证了理论分析的正确性。结果表明模拟采样值延时能够为降低采样值相位误差、同步多路采样值时序、测量报文传输实时性、实现过程层网络配置提供重要依据。

关键词:

A Method to Deal With Packet Transfer Delay of Sampled Value in Smart Substation

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Abstract:

It is the key task for digital communication of process bus in smart substations to accurately propagate the message of sampled analogue value (SAV) to intelligent electronic device (IED) in real-time mode. For this purpose, on the basis of building a time series model to propagate SAV message a method, which improves the time scale of SAV by calibrating the phase delay in merging units (MU) and validates the real-time performance of SAV message by calculating the maximum packet transfer delay within Ethernet, is proposed. Theoretical analysis on the proposed method is performed by formula derivation and example calculation, and the correctness of theoretical analysis is verified by simulation based on OPNET modeler. Simulation results show that the packet transfer delay of SAV can provide important foundation for reduction of phase error of SAV, synchronization of time sequences of multi-channel SAVs, measurement of real-time performance of message communication and the implementation of network configuration for process bus.

Keywords:

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