


[首页](#)
[学院概况](#)
[新闻公告](#)
[师资力量](#)
[人才培养](#)
[科学研究](#)
[学生工作](#)

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职称职务：博士（后）、研究员、博（硕）生导师。2006年获得北作于成都市倍佳实业有限公司技术部，2009年8月获得香港科技大学香港科技大学电子信息与计算机工程博士学位。此后以副研究员的身重庆大学百人计划特聘研究员身份加入重庆大学通信工程学院集成电路数字转换器以及高速通信接口和片上系统芯片设计，在包括IEEE Jou Letters，IEEE Transaction on Electron Devices, IEEE Transaction on Circuits Conference等国内外权威期刊和会议上发表论文。唐枋博士: Systems-II和IEEE Transaction on Biomedical Circuits and Systems Exhibits on Quality Electronic Design，2014年IEEE国际电子器件和

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