

液晶与显示 2014, 29(1) 7-14 ISSN: CN:

本期目录 | 下期目录 | 过刊浏览 | 高级检索

[打印本页] [关闭]

材料物理和化学

大尺寸TFT-LCD ECCP刻蚀工艺低耗整合

张定涛^{1,2}, 李文彬¹, 姚立红¹, 郑云友², 李伟, 袁明²

1. 北京林业大学, 北京 100083;

2. 北京京东方显示技术有限公司, 北京 100176

摘要: 为简化大尺寸液晶面板四次光刻法的刻蚀工艺、减少有毒气体使用、降低射频功率消耗, 在2 200 mm×2 500 mm大尺寸玻璃上, 采用正交实验设计, 验证了功率、气压、反应气体和比例等参数对各刻蚀步骤刻蚀速率、均一性和选择比的影响关系, 从而得到各膜层的最佳工艺条件。在Enhance Cathode Couple Plasma Mode (ECCP) 刻蚀模式下, 采用新刻蚀条件合并薄膜晶体管有源区非晶硅、光刻胶、湿刻后源极和漏极残余金属钼以及沟道非晶硅层干法刻蚀。利用扫描电子显微镜 (SEM) 对薄膜电学特性进行测试, 结果显示, 金属钼的刻蚀可以采用一次两步干法刻蚀, “2干2湿”刻蚀可以整合为“1干1湿”。整合后总刻蚀工艺时间减少16 s, 减少了氯气使用量和RF总功率。试验改进了均一性和刻蚀率, 同时对于下底衬具有良好的选择比, 保持了良好的形貌, 为大批量“1干1湿”生产提供了依据。

关键词: 低耗 钼刻蚀 阴极耦合离子 试验设计

Low consumption combination of large-sized TFT-LCD ECCP etch process

ZHANG Ding-tao^{1,2}, LI Wen-bin¹, YAO Li-hong¹, ZHENG Yun-you², LI Wei, YUAN Ming²

1. Beijing Forestry University, Beijing 100083, China;

2. Beijing BOE Display Technology co., LTD., Beijing 100176, China

Abstract: In order to simplify the lithography 4 mask etch process flow, reduce power and toxic gas volume, an orthogonal experiment was designed based on 2 200 mm×2 500 mm glass substrate. Using scanning electron microscopy (SEM) and electrical properties testing, the interacting influences of power, pressure, gas and gas ratio on etching rate, uniformity and selectivity were analyzed. Meanwhile, the optimized single layer process conditions was verified. Through a novel Enhance Cathode Coupling Plasma Mode (ECCP) etch condition, we combined active, halftone, wet etch residual metal molybdenum and N-plus doped amorphous silicon layers into dry etch fully. The result shows that the metal molybdenum etch can be divided into one time two steps, consequently, "2 dry 2 wet" etching can be modified into "1 dry 1 wet". Total etch time can be reduced by 16 s; overall Cl₂ gas dosage and power were also decreased. Uniformity, etch rate and selectivity to bottom layer were also improved, and the profile was controlled. The study presented a critical basis of "1 dry 1 wet" future mass production.

Keywords: low-consumable molybdenum-etch enhance cathode couple plasma design experiment

收稿日期 2013-07-26 修回日期 2013-09-09 网络版发布日期

基金项目:

通讯作者: 李文彬, E-mail: leewb@bjfu.edu.cn

作者简介: 张定涛(1977-), 男, 河南南阳人, 博士研究生, 研究方向为林业加工装备与技术。E-mail: zhang dingtao@boe.com.cn

作者Email: leewb@bjfu.edu.cn

参考文献:

- [1] Kai Y U. TFT-LCDs as the future leading role in FPD//Process of the International Workshop on the Physics of Semiconductor Devices, Kobe: Ukai Display Device Institute, 2007: 29-34. [2] 谷至华. 薄膜晶体管(TFT)阵列制造技术[M]. 上海: 复旦大学出版社, 2007: 67-69. Gu Z H. Thin Film Transistor Array Manufacturing Technology[M]. Shanghai: Fudan University Press, 2007: 67-69. (in Chinese) [3] Li L, Qin W, Xue J S, et al. Analysis of TFT array erosion defect[J]. Chin. J. Liquid Crystals and Displays, 2010, 25(1): 29-33. [4] Song J H, Kwon D J, Kim S G. Advanced four-mask process architecture for the a-Si TFT array manufacturing method[J]. SID Symposium Digest of Technical Papers, 2002, 3(1): 1038-1041. [5] Li C M, Tian L H, Xu Z, et al. Sputtering of W-Mo alloy under ion bombardment[J]. Transactions of Nonferrous Metals Society of China, 1999, 12(3): 629-633. [6] 李远士, 牛焱, 吴维强. 金属材料的高温氯化腐蚀[J]. 腐蚀科学与防护技术, 2000, 12(1): 41-44. Li Y S, Niu Y, Wu W J. Metal materials, high-temperature chloride corrosion[J]. 2000, 12(1): 41-44. (in Chinese) [7] Muhammad M H, Nairn M, Zhan Z B. Metal wet etch issues and effects in dual metal gate stack integration[J]. Journal of the Electrochemical Society, 2006, 153(5): 43-46. [8] Kang S, Efremov A, Yun, et al. Etching characteristics and mechanisms of Mo and Al₂O₃ thin films in O₂-Cl₂-Ar inductively coupled plasmas: effect of gas mixing ratios[J]. Thin Solid Films, 2004: 552(3): 105-110. [9] Xiong S Z, Zhao Y. Al Ti alloy grid a-Si TFT study[J]. Chin. J. Semiconductors, 1997, 18(10): 771-775. [10] 谢晓强, 戴旭涵, 赵小林, 等. 反应离子刻蚀中的边缘效应及其补偿办法[J]. 真空电子技术, 2005, 12(2): 41-44. Xie X Q, Dai X H, Zhao X L, et al. Reactive ion etching of edge effect and its compensation method[J]. Journal of vacuum electronic technology, 2005, 12(2): 41-44. (in Chinese) [11] Zheng Z R, Chen Z W, Jiang D H, et al. Study on reactive ion etching of Si and SiNx in Cl₂+SF₆ mixtures[J]. Advanced Display, 2008, 19(3): 48-50. [12] 刘翔, 王章涛, 崔祥彦, 等. 液晶阵列四次光刻工艺中光刻胶灰化工艺的研究[J]. 真空科学与技术学报, 2008, 28(4): 291-293. Liu X, Wang Z T, Cui X Y, et al. Photoresist ashing in four-mask fabrication of thin film transistor crystal liquid[J]. Chin. J. Vacuum Science and Technology, 2008, 28(4): 291-293. (in Chinese) [13] 张智胜. 超深亚微米物理设计中天线效应的消除[J]. 半导体技术, 2012, 37(6): 429-432. Zhang Z S. Process antenna effect elimination in ultra deep submicron physical design[J]. Semiconductor Technology, 2012, 37(6): 429-432. (in Chinese) [14] 苟本鹏. 大尺寸面板液晶显示屏阵列工艺中的金属钼刻蚀研究[D]. 成都: 电子科技大学, 2007: 62-70. Xun B H. Large size liquid crystal display panel array of molybdenum metal etching research[D]. Chendu: University of Electronic Science and Technology, 2007: 62-70. (in Chinese) [15] Baek K H, Yun S J, Park J M, et al. The role of Sulfur during Mo etching

using SF₆ and Cl₂ gas chemistries[J]. Journal of Materials Science Letters, 1998, 17(17):1483-1486. [16] 闫方亮, 沈世妃, 侯智, 等. a-Si厚度对TFT开关特性的影响[J]. 现代显示, 2011, 12(7): 23-25. Yan F L, Shen S F, Hou Z, et al. Effect of a-Silicon thickness on TFT characteristic[J]. Advanced Display, 2011, 12(7): 23-25. (in Chinese) [17] 宋跃, 邹雪城. a-Si TFT亚阈特征参数与有源层的厚度效应[J]. 固体电子学研究与进展, 2004, 12(1): 20-25. Song Y, Zou X C. a-Si TFT threshold characteristic parameters with the thickness of the active layer effect[J]. Journal of Solid State Electronics Research and Development, 2004, 12(1): 20-25. (in Chinese) [18] 刘北平, 李晓良, 朱海波. 氯气基气体感应耦合等离子体刻蚀GaN的工艺[J]. 半导体学报, 2006, 27(7): 1335-1338. Liu B P, Li X L, Zhu H B. Cl₂ gases inductively coupled plasma etching process of GaN [J]. Chin. J. Semiconductors, 2006, 27(7): 1335-1338. (in Chinese)

本刊中的类似文章

1. 张定涛. 大尺寸TFT-LCD ECCP刻蚀工艺低耗整合[J]. 液晶与显示, (): 0-0

Copyright by 液晶与显示